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BACKPLANE CONNECTIONS

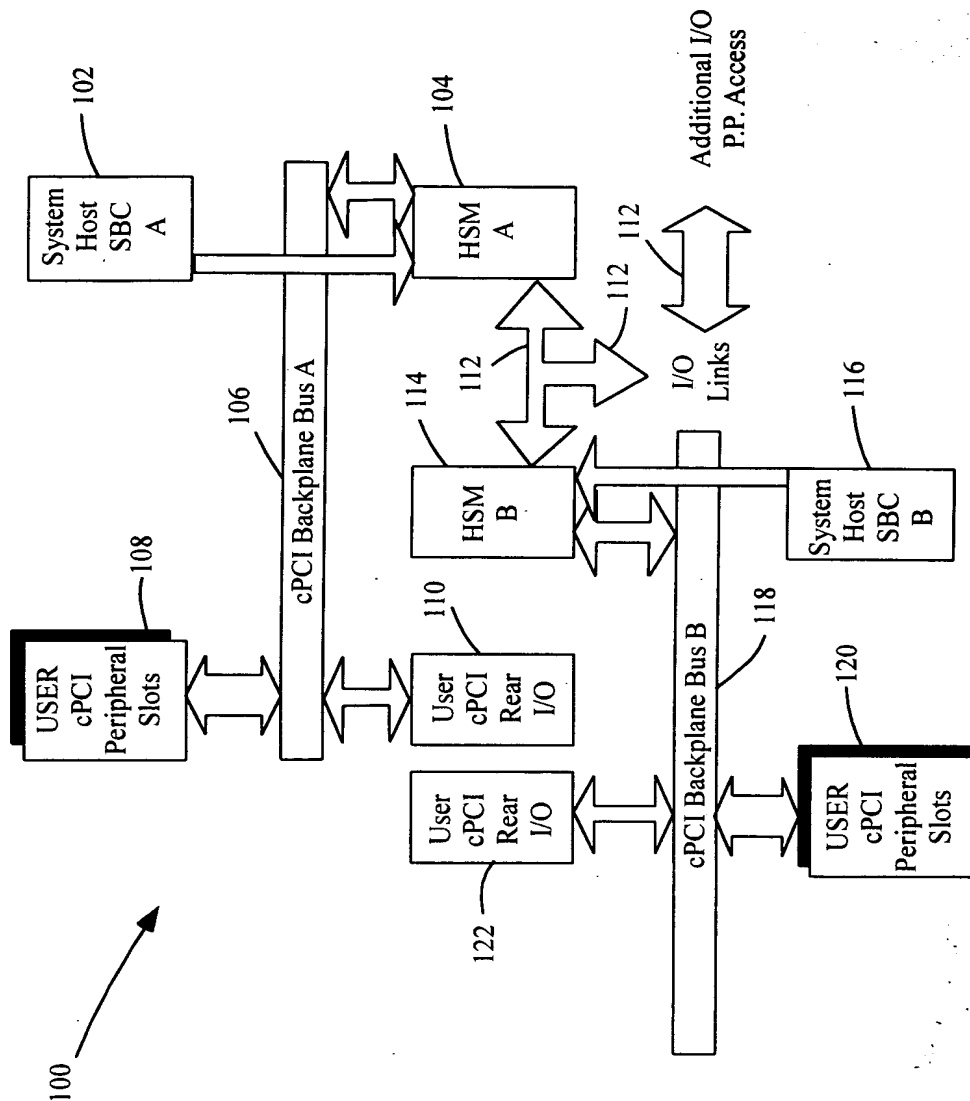


Fig.1

cPCI SIGNALS ROUTING

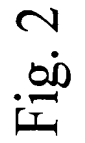


Fig. 2

BACKPLANE DESIGN

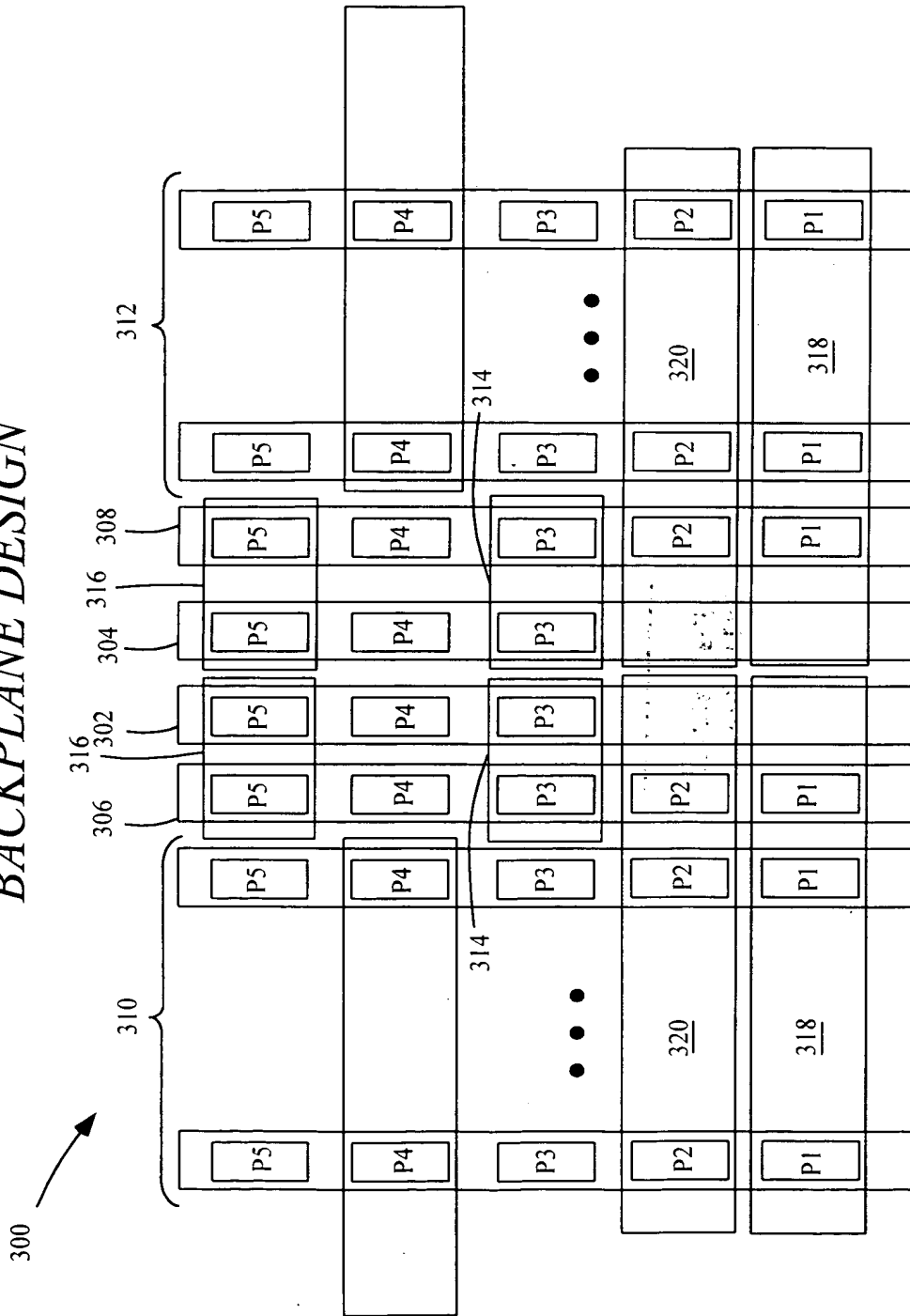


Fig. 3

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Hybrid Switching Module

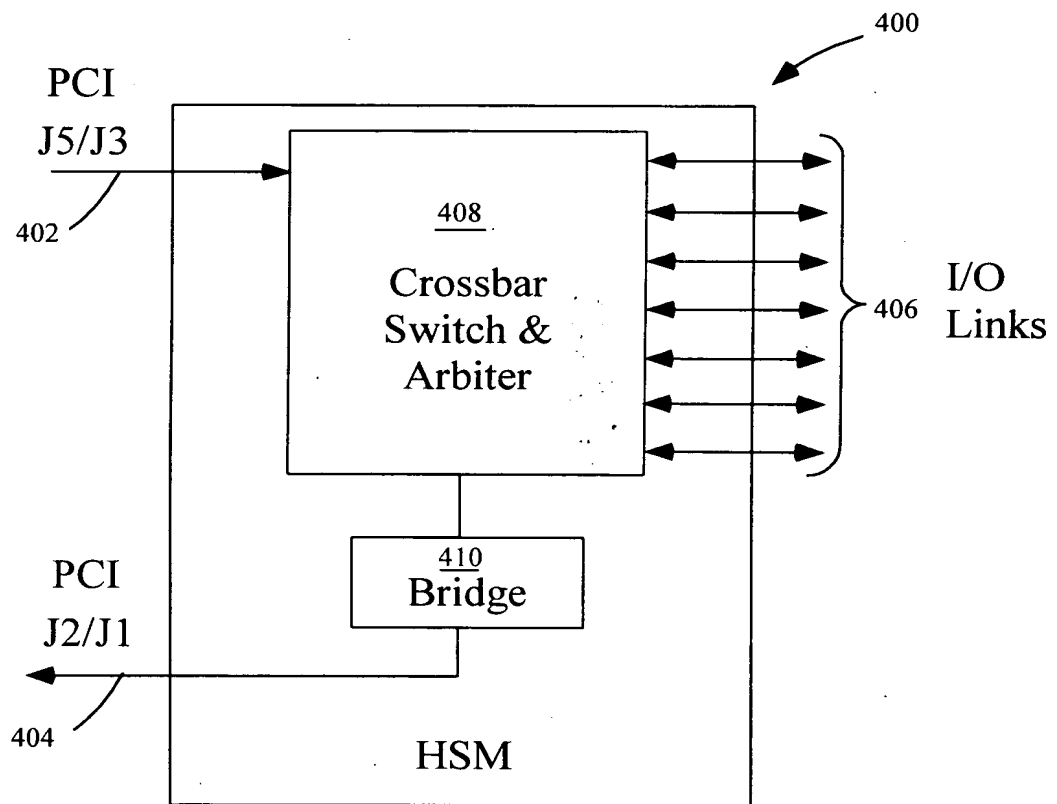


Fig. 4

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PCI-IB Migration

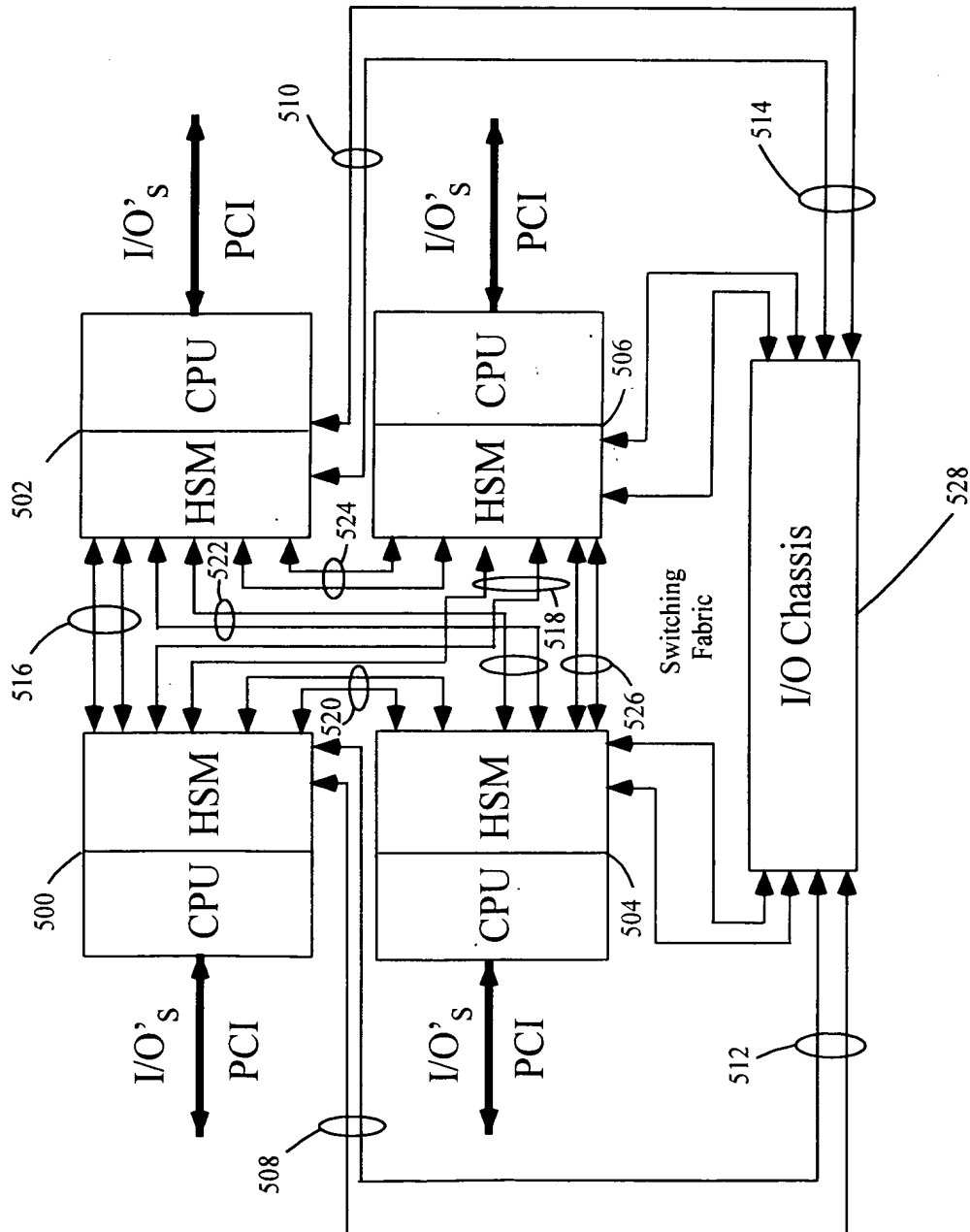


Fig. 5